



Docket No.: 1081.1102

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Masatoshi AKAGAWA ✓

Serial No. 09/754,323 ✓

Confirmation No. 3680

Filed: January 5, 2001 ✓

Group Art Unit: 2823

Examiner: Scott A. Brairton

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREFOR

**LETTER TO THE EXAMINER SUBMITTING
CORRECTED AND/OR FORMAL DRAWINGS**

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicants herewith submit 1 sheet (FIGS. 1A-D) of corrected and/or formal drawings for filing in the subject application.

It is respectfully requested that the corrected and/or formal drawings filed herewith be entered in the above-referenced application.

Respectfully submitted,

STAAS & HALSEY LLP

Date: Sept 18, 2002

By: Patrick J. Starzzone
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CERTIFICATE UNDER 37 CFR 1.9(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231

on September 18, 2002

By: Michael M. Hise

Date: September 18, 2002

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